



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Yoshitaka UEDA et al.**

Group Art Unit: **2816**

Serial Number: **10/760,475**

Examiner: **An T. Lun**

Filed: **January 21, 2004**

Confirmation Number: **9800**

**For: SEMICONDUCTOR INTEGRATED CIRCUIT RELATED TO A
CIRCUIT OPERATING ON THE BASIS OF A CLOCK SIGNAL (As
Herein Amended).**

Attorney Docket Number: **001681A**

Customer Number: **38834**

AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: November 21, 2005

Sir:

This paper is filed in response to the Office Action dated August 23, 2005.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 11 of this paper.